

Pereinamųjų metalų oksidų ALD sluoksnių tyrimai GaN optoelektronikos taikymams

Investigation of ALD transition metal oxide films for GaN optoelectronic's applications

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Anticipation for the oxide barrier layers to fulfill simultaneously a complex of requirements for current spreading, passivation, light extraction, encapsulation, or chemical stability within the process flow of optoelectronic device manufacturing is high. Besides that, the electrical characteristics for the device operation are essential, meaning the stable flat-band, the low leakage current, the high breakdown strength, etc. That's why the efforts of the oxides community pay off with the appearance of new oxides, new bilayer, or even multilayer combinations featuring better, more persistent, qualities.

The atomic layer deposition (ALD) is among the most popular oxide deposition techniques, providing uniform deposition of conformal ultra-thin films with controllable thickness, even on complex three-dimensional surfaces. Not a surprise that the GaN optoelectronic industry has this technique more and more integrated into their production lines of LED, LD, MOS, and other devices.

In this work, we present a study about a series of transition metal oxides, HfO₂, ZrO₂, Ta₂O₅, TiO₂, Nb₂O₅, and Y₂O₃ but also SiO₂ and Al₂O₃ as a reference, ALD deposited on silicon or GaN substrates. The key issue was to focus on thermal and electrical stability. We correlate the oxide layer surface morphology with the post-annealing phase transformation while showing selective crystallization kinetics for the ALD oxide on silicon and GaN, in the view of deposition temperature, and oxidant [1]. A high breakdown field beneficial for insulation layers, we demonstrate for the oxides deposited at a lower temperature of ≤125°C, in particular HfO₂, ZrO₂, Ta₂O₅/H₂O (oxide/oxidant), and Al₂O₃. For the oxide-GaN interface at zero gate voltage and under unstressed condition, we have observed a qualitative feature demonstrating different dependence of the net fixed charge in the oxide or/and at the interface with the deposition temperature, namely, an increase of positive charge for oxidant water and an increase of negative charge for oxidant ozone. Best interface quality, least charge, and only slightly influenced by the deposition temperature, we have observed for the ZrO₂/H₂O. Instead, the highest temperature dependence – observed for the Al₂O₃/H₂O [2]. Comparatively, we discuss the C-V hysteresis resulting from the variation of interface trapping state concentration and their changes in the course of the crystallization. Worth mentioning that the

comparative study on a variety of oxides deposited and investigated was performed on the same trial.

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Keywords: ALD, oxide, film, GaN, silicon, annealing, electrical breakdown, interface.

Literatūra

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